

ERRATUM

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# Erratum to: An optimizing pipeline stall reduction algorithm for power and performance on multi-core CPUs

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## Erratum

In the original published version of this article [1], the third co-author's name Kothari Dwarkadas Pralhaddas appears twice, and the name of the second co-author Alagan Anpalagan is missing. The correct author list is as follows:

Vijayalakshmi Saravanan, Alagan Anpalagan, Kothari Dwarkadas Pralhaddas and Isaac Woungang

The publisher apologies for this error introduced during publication of this article.

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